

[illegible]



EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

FORM PTO-1449 U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use several sheets if necessary)				ATTY. DOCKET NO. I-2-0392.1US		SERIAL NO. 10/676,152	
				APPLICANT Bullan et al.			
				FILING DATE September 29, 2003		GROUP 2611	

U.S. PATENT DOCUMENTS							
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE

FOREIGN PATENT DOCUMENTS								
		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)		
DH		F.M. Garner, "Interpolation in Digital Modems – Part I: Fundamentals," IEEE Transactions on Communications, Vol. 41, No. 3, March 1993, pgs. 501-507.
DH		L. Erup et al., "Interpolation in Digital Modems – Part II: Implementation and Performance," IEEE Transactions On Communications, Vol. 41, No. 6, June 1993, pgs. 998-1008.
DH		Wu, Yik-Chung et al. "FPGA Implementation of Digital Timing Recovery in Software Radio Receiver", Circuits and Systems, 2000. IEEE APCCAS 2000. The 2000 IEEE Asia-Pacific Conference on Dec. 4-6, 2000, Piscataway, NJ USA, IEEE, pp. 703-707.

EXAMINER 	DATE CONSIDERED 11/09/05
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